

A Neuro-inspired Memristive Organic-Nanoparticles Synapse-Transistor (SYNAPSTOR)

F. Alibart*, S. Pleutin*, D. Guerin*, O. Bichler**, Zhao**, C. Gamrat** & D. Vuillaume*.

(*) Institute for Electronics Microelectronics and Nanotechnology (IEMN), CNRS, F-59652 Villeneuve d'Ascq, France.

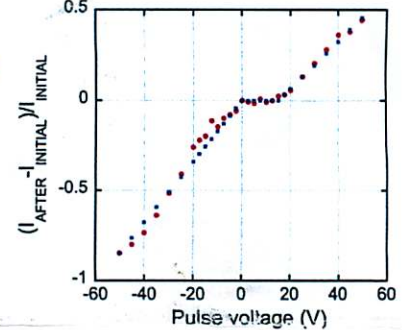
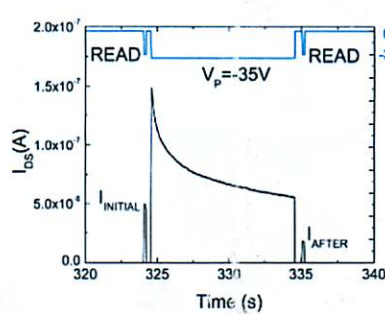
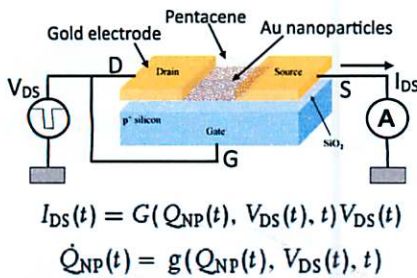
(**) CEA, LIST, Embedded Computers Laboratory, 91191 Gif-sur-Yvette Cedex, France.

A large effort is now devoted to the research of new computing paradigms associated to innovative nanotechnologies that should complement the classical Von Neumann/CMOS association. Among various propositions, spiking neural networks (SNN) seem a valid candidate. A key element for the realization of such networks is the implementation of the synaptic connection and its associated functionalities (synaptic plasticity) with nanoscale devices.

Device realisation:



Memory effect characterization: A memristive model adapted to a « volatile » memory



F. Alibart et al., *Adv. Func. Mater.* 20, 330 (2010)

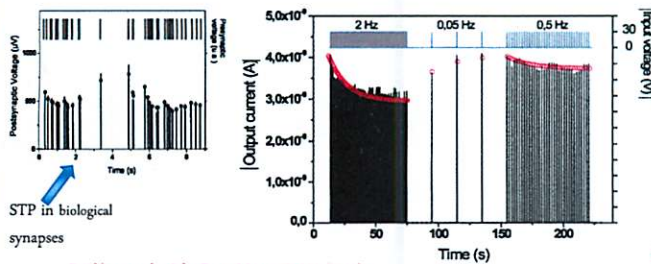
O. Bichler et al., *IEEE Trans. Elec. Dev.* 57, 3115 (2010)

F. Alibart et al., *Adv. Func. Mater.* 22, 609 (2012)

Synaptic Plasticity: Implementation of STP and STDP

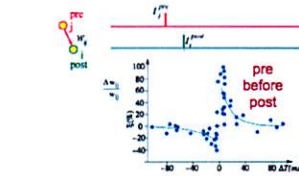
Short Term Plasticity :

Weight depends on input pulses frequency



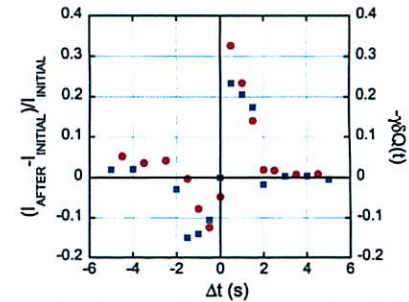
F. Alibart et al., *Adv. Func. Mater.* 20, 330 (2010)

Spike Timing Dependent Plasticity: Weight depends on temporal correlation between input and output pulses



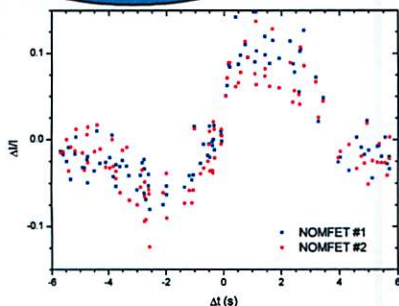
STDP in biological synapses

F. Alibart et al., *Adv. Func. Mater.* 22, 609 (2012)

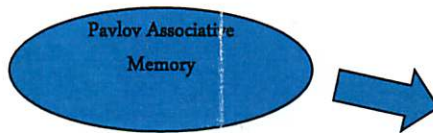


Hybrid CMOS/Synapstor for neuromorphic computing

STDP as a learning algorithm



O. Bichler et al., *Neural Computation* (submitted)



NOMFETs

